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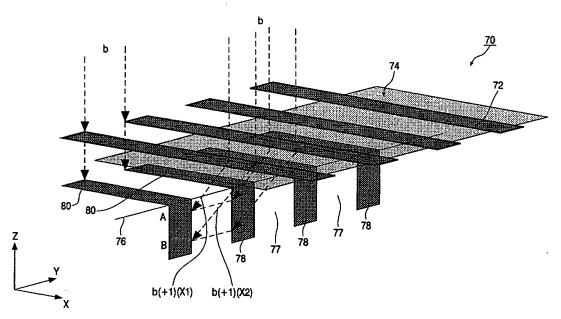
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[Continued on next page]

(54) Title: LITHOGRAPHIC METHOD FOR WIRING A SIDE SURFACE OF A SUBSTRATE



(57) Abstract: In a lithographic proximity method for wiring an end or internal side surface of a substrate the required exposure of strips (76), defining the wiring pattern, is performed by means of a mask (70) comprising a diffraction structure (74) to deflect exposure radiation (b) to the side surface. An exposure beam, which is perpendicularly incident on the mask, is used so that enhanced tolerance for proximity gap width variations is obtained. The method allows manufacture of accurate and fine wiring.



TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

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